

Normally – OFF Silicon Carbide Super Junction Transistor

V _{DS}	=	650 V
$V_{DS(ON)}$	=	1.7 V
I _D	=	4 A
R _{DS(ON)}	=	415 mΩ

Features

- 250 °C maximum operating temperature
- Temperature independent switching performance
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- · Positive temperature coefficient for easy paralleling
- · Low gate charge
- · Low intrinsic capacitance

Package

• RoHS Compliant





SMD0.5 / TO - 276 (Hermetic Package)

Advantages

- Low switching losses
- Higher efficiency
- High temperature operation
- · High short circuit withstand capability

Applications

- Down Hole Oil Drilling, Geothermal Instrumentation
- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- · Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

Maximum Ratings at T_j = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	V_{DS}	V _{GS} = 0 V	650	V
Continuous Drain Current	I _D	T _C = 165 °C	4	Α
Gate Peak Current	I_{GM}		5	Α
Reverse Gate – Source Voltage	V_{GS}		200	V
Reverse Drain – Source Voltage	V_{DS}		40	V
Power Dissipation	P _{tot}	T _C = 25 °C	7	W
Operating and Storage Temperature	T_{j},T_{stg}		-55 to 250	°C

Electrical Characteristics at T_i = 250 °C, unless otherwise specified

Parameter	Symbol	0 1141	Values			
		Conditions	min.	typ.	max.	Unit
On Characteristics						
		I _D = 4 A, I _G = 100 mA, T _j = 25 °C		1.7		
Drain – Source On Voltage	$V_{DS(ON)}$	$I_D = 4 \text{ A}, I_G = 250 \text{ mA}, T_j = 175 °C$		3.2		V
•		$I_D = 4 \text{ A}, I_G = 250 \text{ mA}, T_j = 250 ^{\circ}\text{C}$		4.7		
Drain – Source On Resistance	R _{DS(ON)}	I _D = 4 A, I _G = 100 mA, T _j = 25 °C		415		
		$I_D = 4 \text{ A}, I_G = 250 \text{ mA}, T_i = 175 ^{\circ}\text{C}$		820		mΩ
		$I_D = 4 \text{ A}, I_G = 250 \text{ mA}, T_j = 250 ^{\circ}\text{C}$		1310		
Gate Forward Voltage	$V_{GS(FWD)}$	I _G = 500 mA, T _j = 25 °C		3.3		V
		$I_G = 500 \text{ mA}, T_i = 250 ^{\circ}\text{C}$		3.2		
DC Current Gain	β	$V_{DS} = 5 \text{ V}, I_{D} = 5 \text{ A}, T_{i} = 25 \text{ °C}$		120		
		$V_{DS} = 5 \text{ V}, I_{D} = 5 \text{ A}, T_{j} = 250 ^{\circ}\text{C}$		85		
Off Characteristics						
		$V_R = 650 \text{ V}, V_{GS} = 0 \text{ V}, T_j = 25 ^{\circ}\text{C}$		7		
Drain Leakage Current	I _{DSS}	$V_R = 650 \text{ V}, V_{GS} = 0 \text{ V}, T_i = 175 ^{\circ}\text{C}$		25		nA
		$V_R = 650 \text{ V}, V_{GS} = 0 \text{ V}, T_i = 250 \text{ °C}$		105		



Electrical Characteristics at T_i = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values		11:4	
			min.	typ.	max.	Unit
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = 35 V, V _{GS} = 0 V, f = 1 MHz, T _{vi} = 25 °C		324		pF
Output Capacitance	C_{oss}			45		pF
Reverse Transfer Capacitance	C_{rss}	1 - 1 WH12, 1 _{Vj} - 23 G		45		pF
Switching Characteristics						
Turn On Delay Time	$t_{d(on)}$			5		ns
Rise Time	t _r	$V_{DD} = 400 \text{ V}, I_D = 5 \text{ A},$		15		ns
Turn Off Delay Time	$t_{\sf d(off)}$	$R_{G(on)} = R_{G(off)} = 44 \Omega,$		74		ns
Fall Time	t _f	V _{SS} = -8/15 V,T _j = 175 °C Refer to Figure 10 for gate drive current waveforms		14		ns
Turn-On Energy Per Pulse	E _{on}			24		μJ
Turn-Off Energy Per Pulse	E_{off}			7		μJ
Total Switching Energy	E_{ts}			31		μJ
Turn On Delay Time	$t_{d(on)}$	$V_{DD} = 400 \text{ V, } I_D = 5 \text{ A,}$ $R_{G(on)} = R_{G(off)} = 44 \Omega,$ $V_{GS} = -8/15 \text{ V ,} T_j = 250 \text{ °C}$ Refer to Figure 10 for gate drive current waveforms		9		ns
Rise Time	t _r			24		ns
Turn Off Delay Time	$t_{d(off)}$			114		ns
Fall Time	t _f			17		ns
Turn-On Energy Per Pulse	E _{on}			54		μJ
Turn-Off Energy Per Pulse	E _{off}			10		μJ
Total Switching Energy	E _{ts}			64		μJ
Thermal Characteristics						
Thermal resistance, junction - case	R _{thJC}			1.6		°C/W

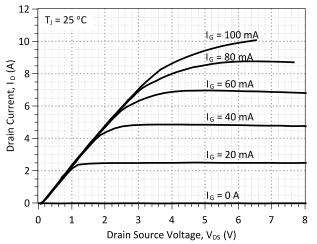


Figure 1: Typical Output Characteristics at 25 °C

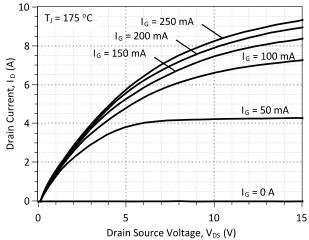


Figure 2: Typical Output Characteristics at 175 °C



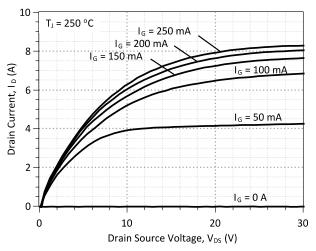


Figure 3: Typical Output Characteristics at 250 °C

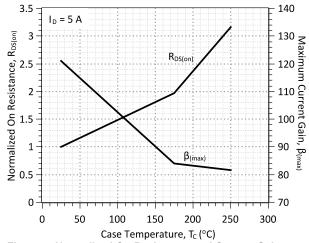


Figure 5: Normalized On-Resistance and Current Gain vs. Temperature

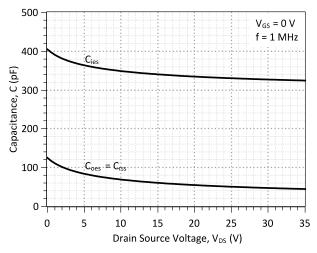


Figure 7: Typical Capacitance vs Drain-Source Voltage

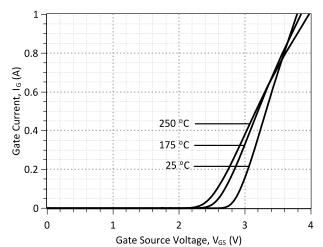


Figure 4: Typical Gate Source I-V Characteristics vs.
Temperature

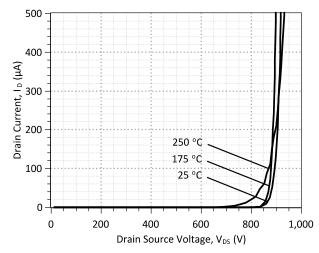


Figure 6: Typical Blocking Characteristics

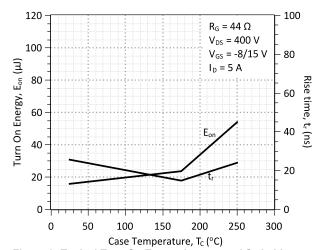


Figure 8: Typical Turn On Energy Losses and Switching Times vs. Temperature



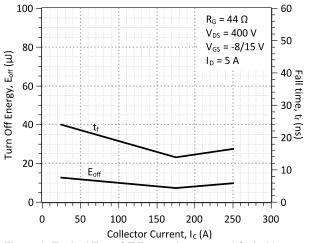


Figure 9: Typical Turn Off Energy Losses and Switching Times vs. Temperature

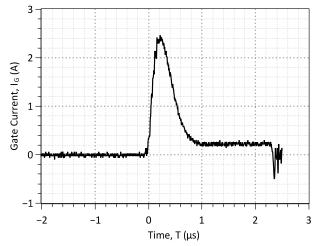
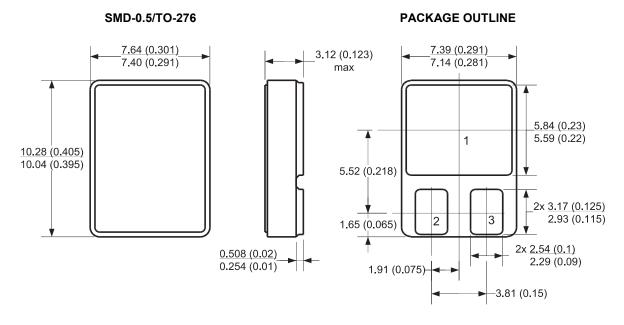


Figure 10: Typical Gate-Source Switching Waveforms

Package Dimensions:



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



Revision History				
Date	Revision	Comments	Supersedes	
2012/08/24	0	Initial release		

Published by GeneSiC Semiconductor, Inc. 43670 Trade Center Place Suite 155 Dulles, VA 20166

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